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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/722,814	11/26/2003	1765	770	29936/39765	2	8	1

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 MARSHALL, GERSTEIN & BORUN LLP
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CONFIRMATION NO. 9355
 CORRECTED FILING RECEIPT



OC000000012230239

APR 05 2004

Date Mailed: 03/30/2004

MARSHALL GERSTEIN

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2003-40155 06/20/2003

If Required, Foreign Filing License Granted: 02/26/2004

Projected Publication Date: 12/23/2004

Non-Publication Request: No

Early Publication Request: No

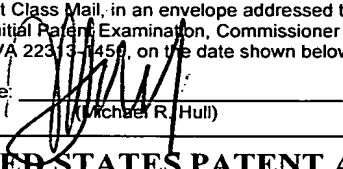
Title

Method of forming gate electrode in semiconductor device

Preliminary Class

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Filing Receipt Corrections, Office of Initial Patent Examination, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: April 8, 2004

Signature: 
(Michael R. Hull)

Docket No.: 29936/39765
(PATENT)

APR 12 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

File Patent Application of:

DONG et al.

Art Unit: 1765

Application No.: 10/722,814

Examiner: Not Yet Assigned

Filed: November 26, 2003

For: METHOD OF FORMING GATE
ELECTRODE IN SEMICONDUCTOR DEVICE

**SECOND REQUEST
FOR CORRECTED FILING RECEIPT**

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
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Dear Sir:

Applicants request issuance of a corrected filing receipt that correctly identifies the city in which the Applicants reside as:

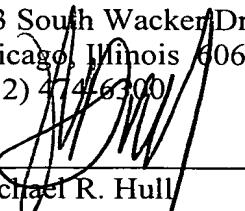
"Cha Deok Dong, Kyungki-Do, KOREA, REPUBLIC OF"
"Ho Min Son, Kyungki-Do, KOREA, REPUBLIC OF"

The filing receipt most recently issued by the Patent and Trademark Office (copy enclosed) incorrectly identifies the city in which the Applicants reside as "Inchon-Shi."

Respectfully submitted,

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April 8, 2004

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